Olivia Chrisley

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Education & Credentials

University of North Florida | Jacksonville, FL *Expected Graduation: December 2026*
Bachelor of Science in Computer Science, 3.62 Major GPA, 3 terms on Dean's List

IBM Z 101 Certificate *September 2025*
Cisco Certified Network Associate (CCNA) *July 2026*

Mainframe Experience

Enterprise COBOL for Fintech (Grade: A) *Fall 2025*

- Started with no mainframe background and learned to work in z/OS, allocating datasets and writing COBOL and JCL in both graphical and 3270 environments
- Finished among the first 3 students to complete IBM's Z Xplore All Star badge
- Researched IBM documentation to trace error messages and work out which options my JCL and config files needed
- Solved real problems from our instructor's fintech career, reflecting the daily operations at companies that run on mainframes
- Noticed classmates needed to work across both the class mainframe and the Z Xplore mainframe; built a Zowe configuration that shows both side by side in VS Code and shared it with the class

Skills

Mainframe: COBOL, JCL, REXX, Assembler, z/OS, ISPF, TSO, SDSF, Zowe, VSAM, DB2, CICS, RACF
Programming: C, Python, JavaScript, TypeScript, HTML, CSS, SQL, Shell scripting
Systems & Networking: Linux (5+ years), Windows, Cisco IOS, VLANs, EtherChannel, IP addressing, Cisco Packet Tracer
Software: GitHub, Docker, Microsoft Office
Professional Organizations: z/OSpreys Mainframe Club, She Can Code IT

Work Experience

Code Ninjas | Jacksonville, FL *February 2025 – Present*
Technology Instructor (Code Sensei)

- Teach programming, robotics, and AI to up to 20 students ages 5-14 weekly, adapting lessons to a wide range of skill levels
- Part of the Sensei team named Most Engaged in the US for 2026, out of 250+ Code Ninjas locations
- Translate complex technical concepts into clear, hands-on projects for learners with no prior background
- Lead tours for prospective families and represent Code Ninjas at community events, explaining the curriculum and learning outcomes to non-technical audiences

Projects

Logic Circuit Simulator, 1st Place Audience Choice, UNF SoC Symposium *Spring 2026*

- Designed a custom HDL with Python-style syntax and a restricted Verilog feature set, including its grammar
- Built an interpreter that compiles circuits into a DAG and computes outputs by propagating signals through components
- Developed a Flask web app that visualizes signal flow through each logic gate

IC Floorplanner *Spring 2026*

- Implemented algorithms from academic literature to compute layout area, using component data from Intel's FloorSet
- Built an interactive PyQt app to watch layouts and cost evolve at each annealing step, for ICs with up to 120 components

Multi-Device Bluetooth Speaker Hub *Fall 2025*

- Researched the Linux Bluetooth and audio stack (BlueZ/bluetoothctl, PulseAudio) to determine how the components interact, since no existing solution supported multi-device audio source switching
- Configured a Raspberry Pi as a discoverable hub, with a script that detects pairing attempts and tracks each device's MAC address and availability in a database; built a web interface that lets users choose which device streams audio

AI For Good Hackathon *Fall 2025*

- Built a logistic regression model in 48 hours, using historical census data and open-source geographic boundaries to predict neighborhoods at high risk of housing displacement
- Presented findings to a panel of local business and community organization representatives